

Integrated and Tool-Supported Teaching of Testing, Debugging, and Verification

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- authors:
‘Software Engineering with Formal Methods’ group at Chalmers

Background

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 - 'Software Engineering with Formal Methods' group at Chalmers
- co-developed **KeY tool** for deductive **JAVA source code verification**

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this talk: conception of the latter

Chalmers University of Technology



- **Strong** engineering tradition, most Swedish engineers from Chalmers
- Emphasis on traditional math courses: calculus, algebra, statistics
- Computing courses **focus on programming**
- on Bachelor level:
 - No** dedicated courses on theoretical computer science topics

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FMs as **integrated** aspect of quality code construction

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We present **spectrum** of quality ensuring activities:
error **detection**, error **elimination**, ensuring error **freedom**

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All methods in action with executable programs

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Formalisation \Rightarrow Tool Support

Formalisation prerequisite for far-reaching analysis tools

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Tools are essential

Without tools potential of formalisation not fully realised

The TDV Course Structure



Specification
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 - `Object` contract requires:
 - `hashCode()` consistent with `equals()`

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 - All classes inherit contract from `Object`
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 - Programmers/students **typically break** that contract

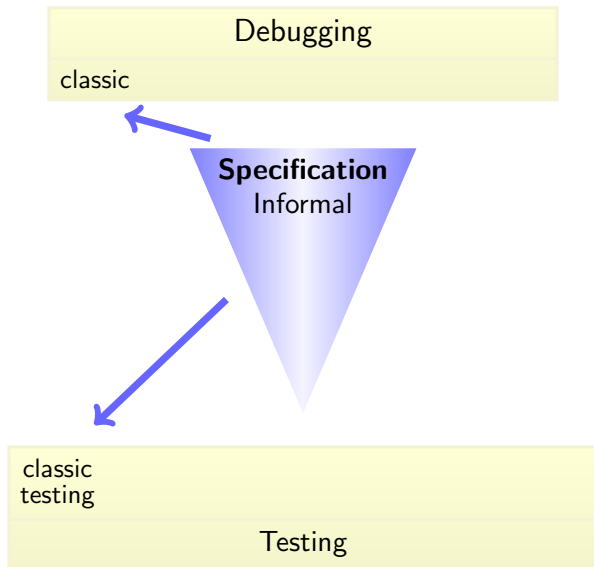
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 - All classes inherit contract from `Object`
 - `Object` contract requires:
 - `hashCode()` consistent with `equals()`
 - Programmers/students **typically break** that contract
 - Consequence: collection classes malfunction

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test inputs and test oracles based on informal specification

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Tool: JUnit

Debugging

Disregarded by most software engineering lectures, in contrast to development time actually spent on debugging.

A. Zeller *Why Programs Fail: A Guide to Systematic Debugging*.
Morgan Kaufmann, Oct. 2005.

Classic Debugging Techniques

- Logging of events
- Controlled execution: Step into/over, breakpoints
- Inspection: variable values, heap inspection

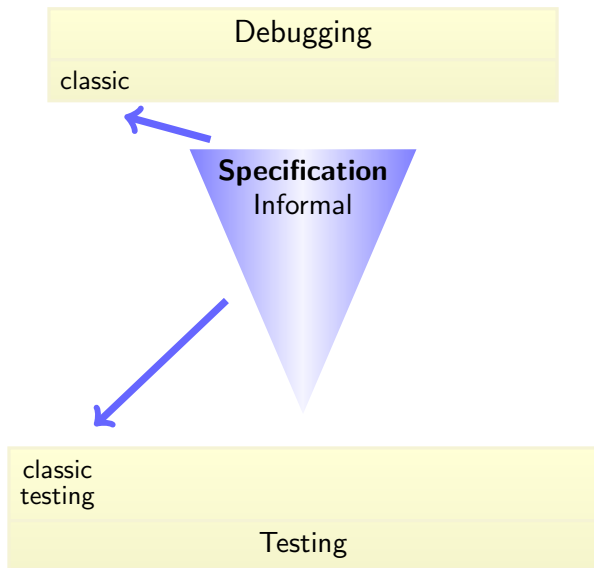
Tools: log4j, eclipse debugger

Delta Debugging

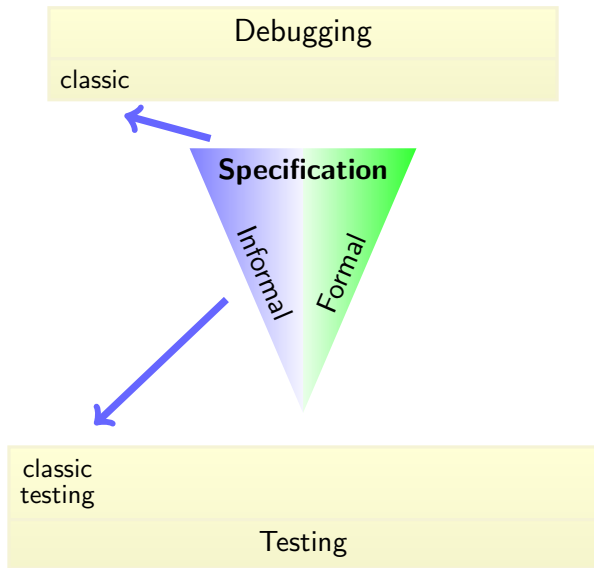
- Automatic retrieval of minimal input triggering the bug

Tool: DDinput

The TDV Course Structure



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Students learn

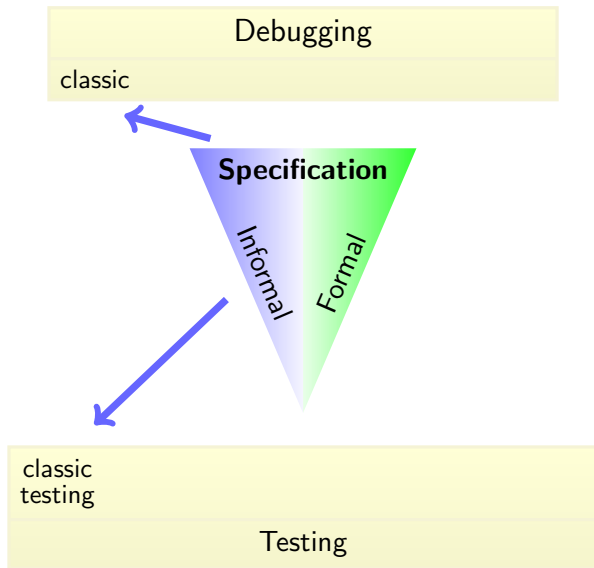
- Formalisation of real-world problems,
- Basics of first-order logic and
- Java Modelling Language (JML) as specification language

Tools: jml (syntax and type checker, COMMON JML TOOLS)

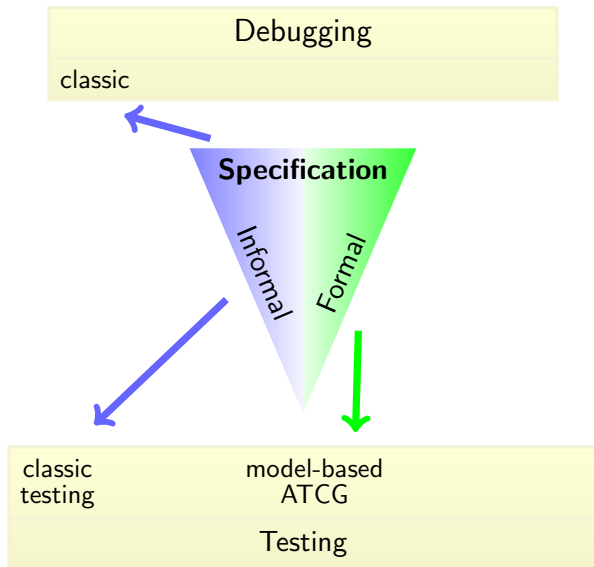
Formal specification prerequisite for **automation of**

- Test generation
- Symbolic debugging
- Formal verification

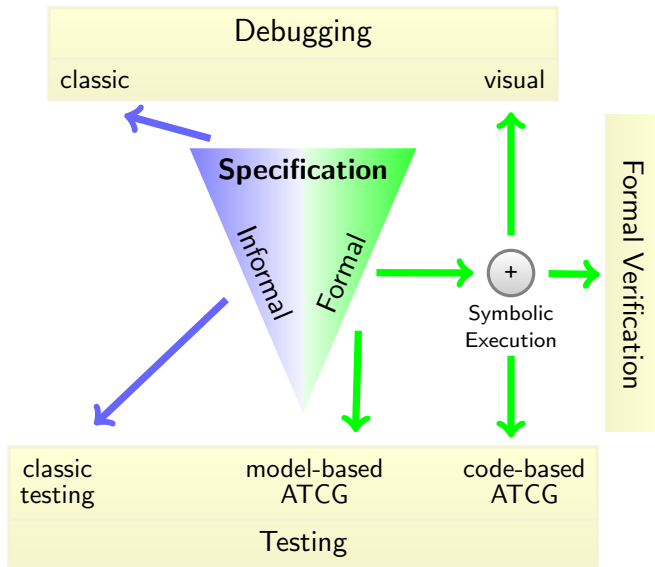
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Blackbox Testing: Model-based testing

Based on formal specification

- Coverage criteria incl. specifications
- Derivation of test scenarios/cases by disjunction analysis
- Deriving test cases from JML specifications

Tool: jmlunit (COMMON JML TOOLS)

Automatic Test Generation

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White-box testing

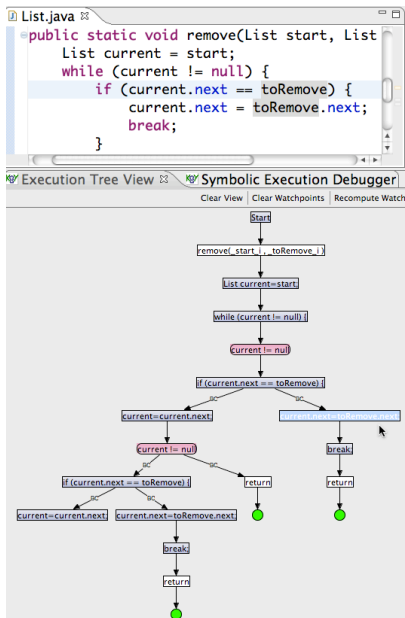
Test cases derived from

- Formal specification **and**
- Source code

Introducing **symbolic execution** as basic technology.

Tool: KeY-VBT

Symbolic Debugging

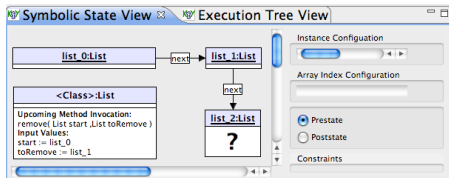


Based on symbolic execution

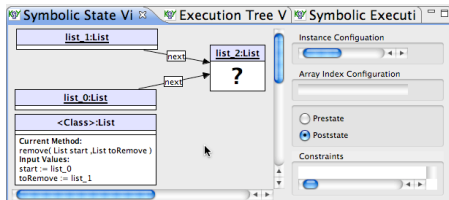
- Covers **all** possible execution paths
- **No** initialisation necessary
- **Efficient** omniscient debugging

Symbolic Debugging

Before removal:



After removal:

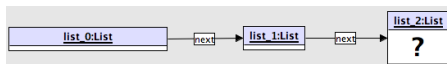


Based on symbolic execution

- Covers **all** possible execution paths
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- Symbolic heap inspection

Symbolic Debugging

Intended configuration:



Unintended configuration:



Based on symbolic execution

- Covers **all** possible execution paths
- **No** initialisation necessary
- **Efficient** omniscient debugging
- Symbolic heap inspection
 - Specification constrains valid heap configuration

Verification

Most formal approach taught in TDV.

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Calculus and tool developed specifically for that course.

Hoare calculus with explicit substitutions

Hoare calculus variant based on **symbolic execution**

- forward reasoning
- elimination of most non-deterministic rules
- first-order reasoning as black-box

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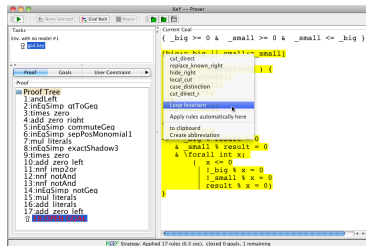
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Tool: KeY-Hoare

- Interactive and automatic verification system (based on KeY)
- Powerful first-order/arithmetic proving capabilities
- Supports partial, total, and execution time aware correctness



Experiences and Discussion

Course given first time: Summer 2007

Course Name: Program verification

Participants: 15 students

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- Missing:
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 - Integration into software development process (**planned!**)

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Course adapted by U. of Innsbruck, TU of Madrid and U. of Freiburg

Research-Driven Course Development

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- our research objective is precisely increased accessibility of FMs
- students profit from this objective
- we profit from increased pressure on usability

in spite of the close connection to our own research:

course can be run in any other context

- All tools freely available, mostly open source
- We actively support adaptation of course (units)
- Course parts adapted at:
 - Technical University of Madrid
 - University of Innsbruck
 - University of Freiburg
- Feedback from adaptations improved our course
(e.g. worst-case execution time in KeY-Hoare
suggested by Joanna Chimiak-Opoka, Innsbruck)

Overview: Tool-Based Teaching

Teaching Unit	Content	Formal	Tools
Testing	Systematic testing, specification, assertions, black/white box, path/code coverage	no	JUNIT
Debugging	Bug tracking, execution control, failure input minimisation, logging, slicing	no	DDinput, ECLIPSE, log4j
Formal Specification	Design-by-contract, formalisation, first-order logic, JML	yes	jml (type checker)
Automated Test Case Generation	Model-based TC generation, Symbolic execution, Code-based TC generation	yes	jmlunit, KEY VSD, KEY VBT
Formal Verification	Hoare triple, weakest precondition, formal verification, loop invariant	yes	KeY-Hoare

All tools freely available software and most open source.